



MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)
(An UGC Autonomous Institution, Affiliated to JNTUH, Hyderabad Accredited 2nd time by NAAC
with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1)
Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,
Telangana State – 500100
www.mrec.ac.in

Department of Electronics and Communication Engineering

CIRCULAR

Date: 10/02/2017

All the students are hereby informed that Value Added Course on “**Mixed Signal Design & Verification Using Cadence Tools**” on 24th to 28th February, 2017 is being organized by the Electronics and Communication Engineering department. The resource person for the course is “**Mr.N.MADHUSUDAN REDDY, FPGA & ASIC INFOTECH PVT LTD, HYDERABAD**”.


Students are advised to register their names to the programme coordinator “**V.Srinivas**”, on or before 22nd February 2017 and utilize this opportunity to enhance their skills by attending the program.

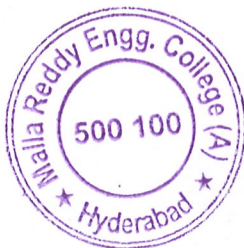
The detailed schedule of the programme will be displayed in the notice board.


Head of the Department

Copy to:

- 1) Circulation in Students classroom
- 2) All HOD's
- 3) Notice Boards
- 4) PA to principal for filing.


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Target Group:

This FDP is open to all the teachers of **Electronics and Communication Engineering** who have interest in learning, teaching and research on the Mixed Signal Design and verification and also Research Scholars and Industry personnel working in Design and Verification, can also register for this programme.

Registration:

Candidates can register by sending the Registration form attached in this brochure along with a **Registration fee of Rs 300** in the form of DD drawn in favor of "Principal, **MREC (Autonomous)**" payable at **Hyderabad**.

Transportation:

Participants can avail the Institute bus facility from different locations of the twin cities

Important Date:

Last Date for submission of Registration form by post/email: **22nd February 2015**.

Registration form can be downloaded from:
www.mrec.ac.in

Address for Correspondence

Mr.V.Srinivas

Assoc Professor, ECE Department

Malla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapally Post,

Via Kompally, Secunderabad-500100

Email: vsrinivas@mrec.ac.in

Mobile:8919643583

Organizing Committee

Chief Patron:

Sri Ch. Malla Reddy, MP
Founder Chairman, CMR Educational Society

Patrons:

Sri Ch. Mahender Reddy
Secretary, CMR Educational Society
Dr. Ch. Bhadra Reddy
Secretary, MRIMS

Co-Patrons:

Dr. S. Sudhakar Reddy, Principal, MREC (A)
Mr.P.Santhosh Reddy, Director, MREC(A)

Convener:

P.S.INDRANI, Professor & Head, ECE Dept.

Co-Ordinators:

Mr.V.Srinivas, Assoc. Professor, ECE Dept

Members:

Dr. Kesavan Gopal, Professor, ECE

Mr. P. Ashok Babu, Assoc Professor, ECE

Mrs C. Silpa, Assoc Professor, ECE

Mr.B.Srinivas, Assoc Professor, ECE

Mrs P. Sowjanya, Assoc Professor, ECE

Mrs G.Bharathi Subhashini, Assoc Professor,
ECE

Mr.CH.Bhanu Prakash, Assoc Professor, ECE

Mr.T.Srinivas Reddy, Assoc Professor, ECE

Mr.T.Surender Reddy, Assoc Professor, ECE

Mr.G Prasanna Kumar, Asst Professor, ECE

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One week Workshop
on
Mixed Signal Design & Verification Using
Cadence Tools

24th to 28th February 2017



Coordinator
Mr.V.Srinivas

Assoc Professor, ECE Department

Convener
P.S.INDRANI

Professor & Head, ECE Department



Organized by
Department of Electronics and
Communication Engineering (ECE)

Malla Reddy Engineering College
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Maisammaguda, Secunderabad – 500 100

Phone: 040-65864982, 040-64634234.

Fax: 040-23090485

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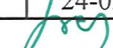
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Students Registered for Value Added Course

2016-17

S.No	Branch	Roll Number	Student Name	Year/ Sem	Value Added Course	DATE OF INTRODUCTI	DATE OF COMPLETIO
1	ECE	14J41A0411	C PRANAYA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
2	ECE	14J41A0413	G SHARATH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
3	ECE	14J41A0421	J AJITH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
4	ECE	14J41A0423	K SAI VIKAS	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
5	ECE	14J41A0424	KAKARLA BINDU	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
6	ECE	14J41A0425	KATI KI SAI KRISHNA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
7	ECE	14J41A0428	KONDUR ADARSH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
8	ECE	14J41A0437	METTU PRIYANKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
9	ECE	14J41A0450	SEEMA BEGUM	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
10	ECE	14J41A0451	SHAIK YOUNIS	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
11	ECE	14J41A0452	T JYOTHI PRIYA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
12	ECE	14J41A0453	T SAI KUMAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
13	ECE	14J41A0460	YAMINI GAJULA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
14	ECE	14J41A0465	ASAM SHRAVYA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017

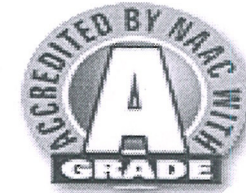
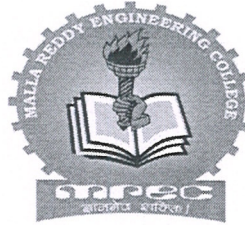
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15	ECE	14J41A0467	CH RAJESH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
16	ECE	14J41A0468	CH SATHVIK	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
17	ECE	14J41A0474	G PUNEETH RAO	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
18	ECE	14J41A0475	GAJJELA CHITRA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
19	ECE	14J41A0476	GOLLA KEERTHI	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
20	ECE	14J41A0479	K REVANTH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
21	ECE	14J41A0480	K SAI KAMAL	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
22	ECE	14J41A0487	MADHA RENUKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
23	ECE	14J41A0489	MEDISHETTI RAMESH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
24	ECE	14J41A0494	MOTHE SRINATH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
25	ECE	14J41A0495	MUTLURI EMMANUEL	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
26	ECE	14J41A04C1	A LIKITHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
27	ECE	14J41A04D2	D PRASHANTH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
28	ECE	14J41A04D3	D SHIVA KRISHNA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
29	ECE	14J41A04D4	D SHIVA KUMAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
30	ECE	14J41A04E3	K BALA KRISHNA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
31	ECE	14J41A04E4	K BHUVAN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017
32	ECE	14J41A04G6	R S PRAVALLIKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2017	28-02-2017

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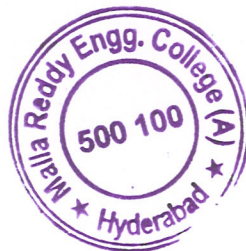
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Certificate of Completion

This is to certify that Mrs./Ms./Mr ARE NARESH bearing Roll Number 15J41A0402 has successfully completed Value added Course in “Mixed Signal Design & Verification using Cadence Tools” Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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Coordinator

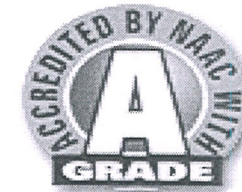


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This is to certify that Mrs./Ms./Mr B AKASH bearing Roll Number 15J41A0403 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

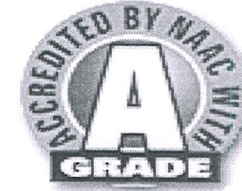
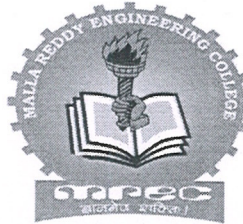
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This is to certify that Mrs./Ms./Mr CH SAITEJA bearing Roll Number 15J41A0406 has successfully completed Value added Course in “Mixed Signal Design & Verification using Cadence Tools” Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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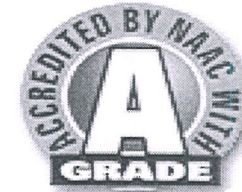
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This is to certify that Mrs./Ms./Mr DODDA DHARANI bearing Roll Number 15J41A0411 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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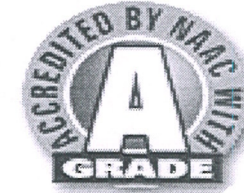


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This is to certify that Mrs./Ms./Mr GUTHA LAVANYA bearing Roll Number 15J41A0418 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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Dr. Srinivas

Coordinator

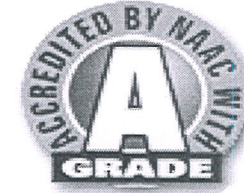


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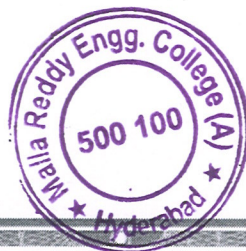
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This is to certify that Mrs./Ms./Mr IREDDY SNEHA bearing Roll Number 15J41A0421 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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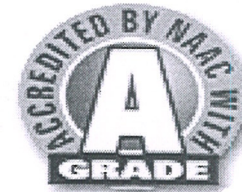
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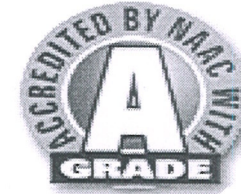
This is to certify that Mrs./Ms./Mr KADARI SANTHOSHINI bearing Roll Number 15J41A0423 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017 *Reveed*

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Certificate of Completion

This is to certify that Mrs./Ms./Mr KAPAVARAPU SAIRAM bearing Roll Number 15J41A0424 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

Dr. Srinivas

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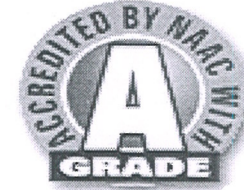
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This is to certify that Mrs./Ms./Mr KOTTURU SANJANA bearing Roll Number 15J41A0428 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

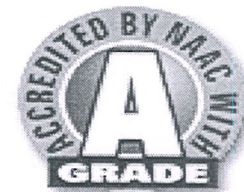
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This is to certify that Mrs./Ms./Mr KUKKA NAVEEN bearing Roll Number 15J41A0429 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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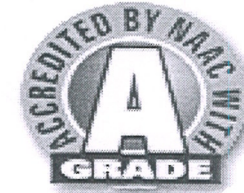
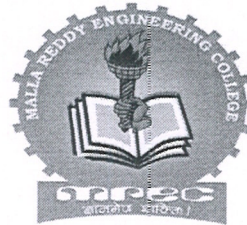
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This is to certify that Mrs./Ms./Mr MUDAM RAMYA bearing Roll Number 15J41A0436 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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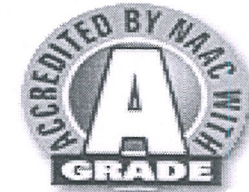


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This is to certify that Mrs./Ms./Mr SHAIK SAJEEDA bearing Roll Number 15J41A0449 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

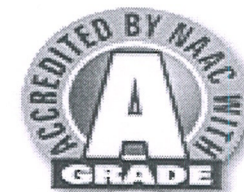
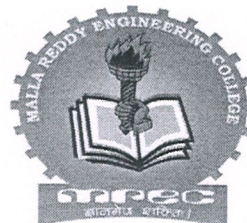
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr TADEM SAI PAVAN bearing Roll Number 15J41A0450 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

[Signature]

Coordinator



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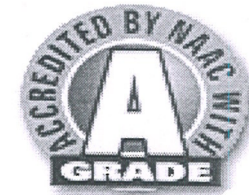
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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr B NIKHILA bearing Roll Number 15J41A0465 has successfully completed Value added Course in "Mixed Signal Design & Verification using Cadence Tools" Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017

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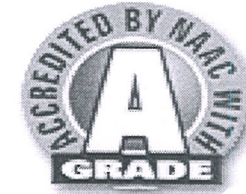


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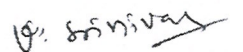
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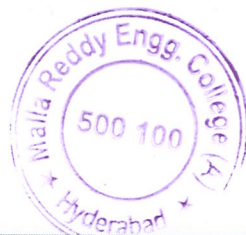
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Certificate of Completion

This is to certify that Mrs./Ms./Mr L S S S SASHANK bearing Roll Number 15J41A0491 has successfully completed Value added Course in “Mixed Signal Design & Verification using Cadence Tools” Organized by the Department of Electronics and Communication Engineering from 24.02.2017 to 28.02.2017


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